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7590 04/22/2005

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Susan J. Scott
(Signature)
5/19/05
(Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/039,045 | 12/31/2001 | Hahn Vo | H052617.1142US0 | 2278 |

TITLE OF INVENTION: LOW LATENCY LOCK FOR MULTIPROCESSOR COMPUTER SYSTEM

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|----------------|-----------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$1700 | 07/22/2005 |
| EXAMINER | ART UNIT | CLASS-SUBCLASS | | | |
| LI, ZHUO H | 2189 | 711-152000 | | | |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
 Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Houston, Texas

Hewlett-Packard Development Company, L.P.

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date 5/19/05

Typed or printed name Gerald E. Laws

Registration No. 39,268

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